DATASHEET

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EL2099

Video Distribution Amplifier

FN7041
Rev 0.00
January 1996

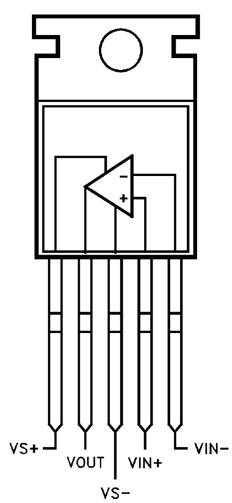
The EL2099 is a high speed, monolithic operational amplifier* featuring excellent video performance and high output current capability. Built using Elantec's Complementary Bipolar process, the EL2099 uses current mode feedback to achieve wide bandwidth, and is stable in unity gain configuration.

Operation from power supplies ranging from ±5V to ±15V makes the EL2099 extremely versatile. With supplies at ±15V, the EL2099 can deliver ±11V into 25Ω at slew rates of 1000V/µs. At ±5V supplies, output voltage range is ±3V into 25Ω. Its speed and output current capability make this device ideal for video line driver and automatic test equipment applications.

Differential Gain and Phase of the EL2099 are 0.03% and 0.05° respectively, and -3dB bandwidth is 50MHz. These features make the EL2099 especially well suited for video distribution applications.

Pinout

EL2099
(5-PIN TO-220)
TOP VIEW



Manufactured under U.S. Patent Nos. 5,179,355, 4,893,091, U.K. Patent No. 2261786.

Features

- 50MHz -3dB bandwidth, A_V = +2
- Differential gain 0.03%
- Differential phase 0.05°
- Output short circuit current 800mA
- Can drive six 75Ω double terminated cables ±11V
- Slew rate = 1000V/µs
- Wide supply voltage range ±5V to ±15V

Applications

- Video line driver
- ATE pin driver
- High speed data acquisition

Ordering Information

PART NUMBER	TEMP. RANGE	PACKAGE	PKG. NO.
EL2099CT	0°C to +75°C	5-Pin TO-220	MDP0028

Absolute Maximum Ratings (T_A = 25°C)

Voltage between V_S+ and V_S- . . . . +33V
Voltage at V_S+ . . . . +16.5V
Voltage at V_S- . . . . -16.5V
Voltage between V_{IN}+ and V_{IN}- . . . . ±6V
Current into V_{IN}+ or V_{IN}- . . . . ±10mA
Internal Power Dissipation . . . . See Curves
Operating Ambient Temperature Range . . . . 0°C to +75°C
Operating Junction Temperature . . . . 150°C
Storage Temperature Range . . . . -65°C to +150°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

Open-Loop DC Electrical Specifications

V_S = ±15V, R_L = 25Ω, T_A = 25°C unless otherwise specified

PARAMETER	DESCRIPTION	TEMP	MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	25°C		5	20	mV
		T_{MIN}, T_{MAX}			25	mV
TC V_{OS}	Average Offset Voltage Drift	Full		20		µV/°C
$+I_{IN}$	+Input Current	25°C		5	20	µA
		T_{MIN}, T_{MAX}			30	µA
$-I_{IN}$	-Input Current	25°C		8	35	µA
		T_{MIN}, T_{MAX}			50	µA
CMRR	Common Mode Rejection Ratio (Note 1)	25°C	50	60		dB
PSRR	Power Supply Rejection Ratio (Note 2)	25°C	60	70		dB
R_{OL}	Transimpedance	25°C	85	140		kΩ
$+R_{IN}$	+Input Resistance (Note 3)	25°C	700	1000		kΩ
		T_{MIN}, T_{MAX}	600			kΩ
$+C_{IN}$	+Input Capacitance	25°C		3		pF
CMIR	Common Mode Input Range	25°C		±12.5		V
V_O	Output Voltage Swing V_S = ±15V	25°C	±9	±11		V
	Output Voltage Swing V_S = ±5V	25°C	±2.4	±3.0		V
I_{OUT}	Output Current	25°C	360	440		mA
I_{SC}	Output Short-Circuit Current	25°C	600	800		mA
		T_{MIN}, T_{MAX}		800		mA
I_S	Supply Current	25°C		32	45	mA

NOTES:

1. The input is moved from -10V to +10V.
2. The supplies are moved from ±5V to ±15V.
3. V_{IN} = ±5V. See typical performance curve for larger values of V_{IN}.

Closed-Loop AC Electrical Specifications

V_S = ±15V, A_V = +2, R_F = 510Ω, R_L = 25Ω, T_A = 25°C unless otherwise specified

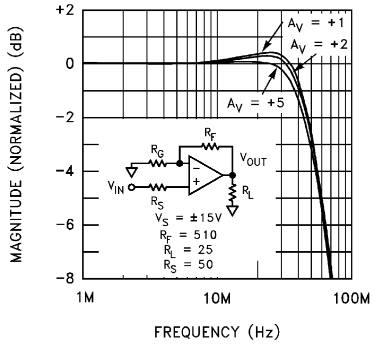
PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNITS
SR	Slew Rate (Note 1) (Note 2)	500	1000		V/µs
BW	-3dB Bandwidth (Note 2)		50		MHz
Peaking	(Note 2)		0.3		dB
t_R, t_F	Rise Time, Fall Time (Note 2) (Note 3)		7		ns
dG	Differential Gain; DC Input Offset from 0V through +0.714V, AC Amplitude 286mV_{P-P}, f = 3.58MHz (Note 4) (Note 2)		0.03		%
dP	Differential Phase; DC Input Offset from 0V through +0.714V, AC Amplitude 286mV_{P-P}, f = 3.58MHz (Note 2) (Note 4)		0.05		deg. (°)

NOTES:

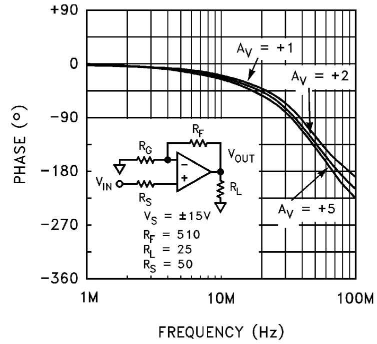
1. Slew Rate is with V_{OUT} from +5V to -5V and measured at 20% and 80%.
2. All AC tests are performed on a “warmed up” part, except for Slew Rate, which is pulse tested.
3. Rise and Fall Times are with V_{OUT} between -0.5V and +0.5V and measured at 10% and 90%.
4. See typical performance curves for other conditions.

Typical Performance Curves (T_A = 25°C, R_L = 25Ω, A_V = +2, R_F = 510 unless otherwise specified)

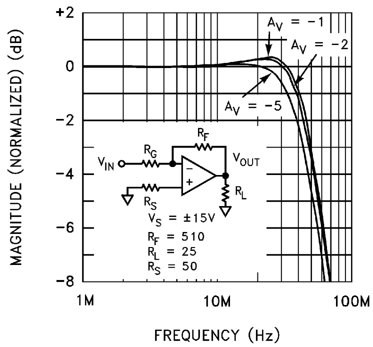
Non-Inverting Frequency Response (GAIN)



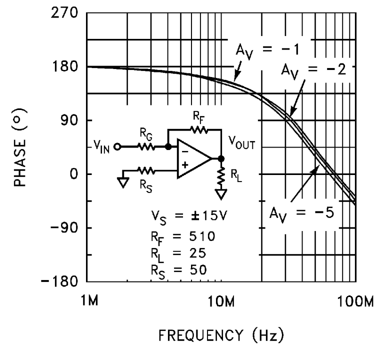
Non-Inverting Frequency Response (PHASE)



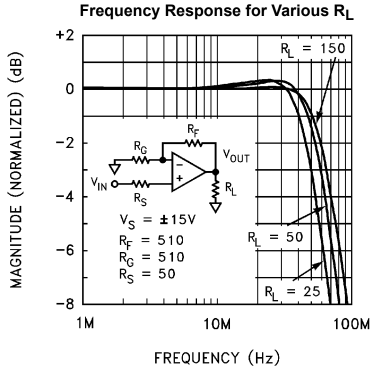
Inverting Frequency Response (GAIN)



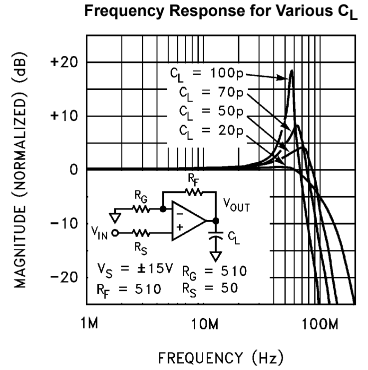
Inverting Frequency Response (PHASE)



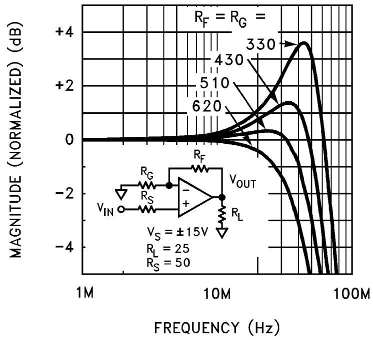
Frequency Response for Various R_L



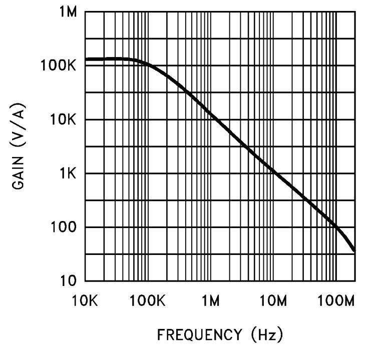
Frequency Response for Various C_L



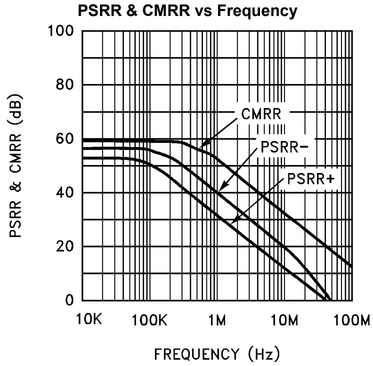
Typical Performance Curves (Continued)



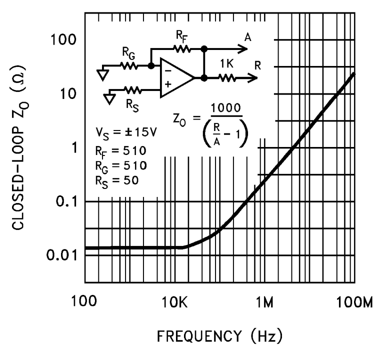
Frequency Response for Various R_F & R_G



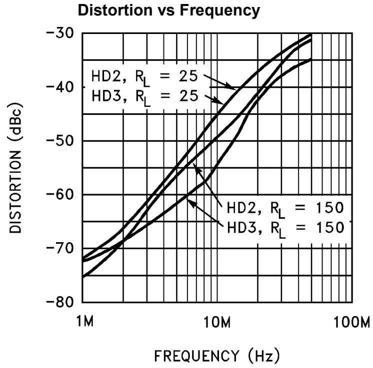
Transimpedance (R_{OL})



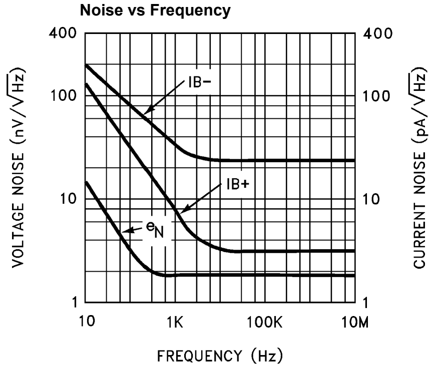
PSRR & CMRR vs Frequency



Closed-Loop Output Impedance vs Frequency



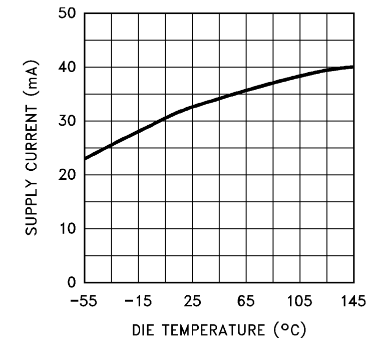
2nd and 3rd Harmonic Distortion vs Frequency



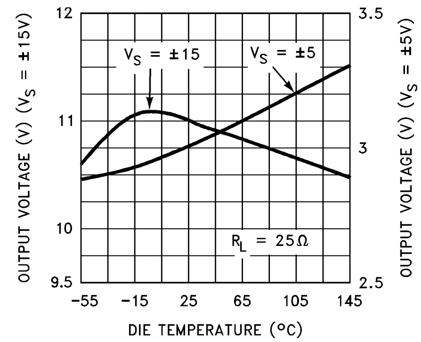
Voltage and Current Noise vs Frequency

Typical Performance Curves (Continued)

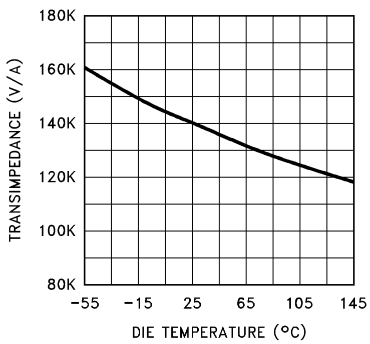
Supply Current vs Die Temperature



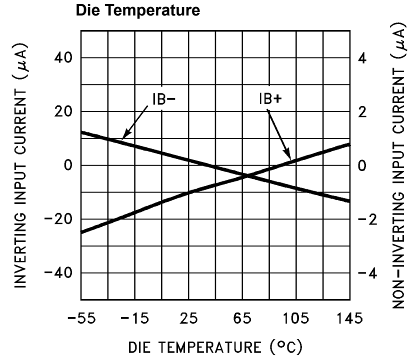
Output Voltage vs Die Temperature



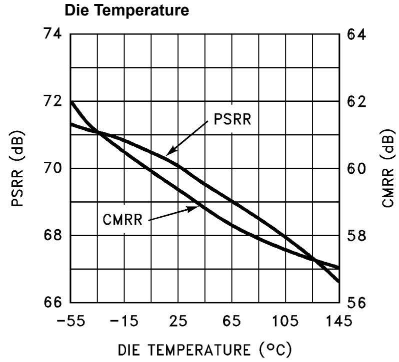
Transimpedance (R_{OL}) vs Die Temperature



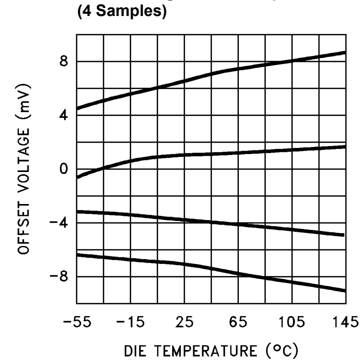
Input Current vs Die Temperature



PSRR & CMRR vs Die Temperature

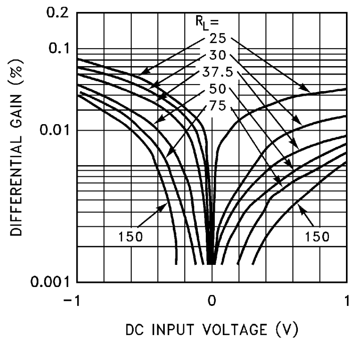


Offset Voltage vs Die Temperature (4 Samples)

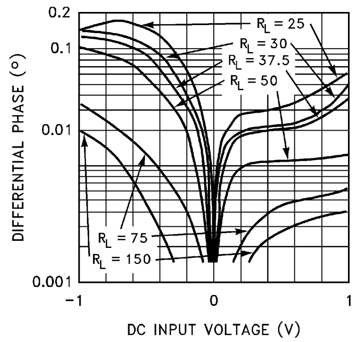


Typical Performance Curves (Continued)

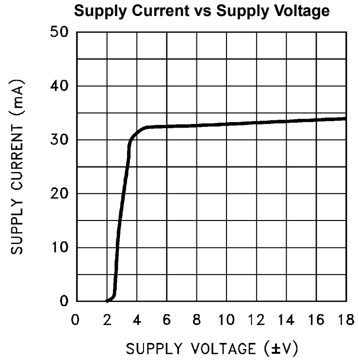
Differential Gain vs DC Input Voltage for Various R_Ls



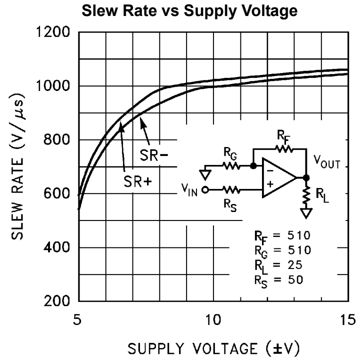
Differential Phase vs DC Input Voltage for Various R_Ls



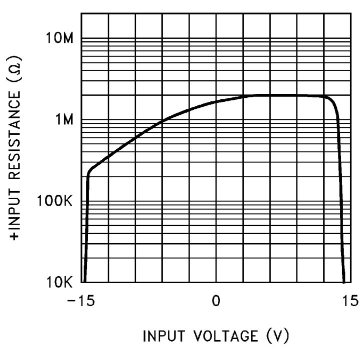
Supply Current vs Supply Voltage



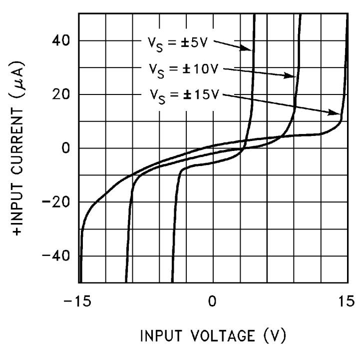
Slew Rate vs Supply Voltage



+Input Resistance vs Input Voltage

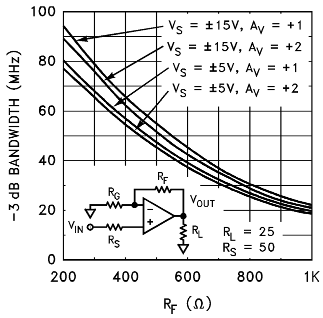


+Input Bias Current vs Input Voltage

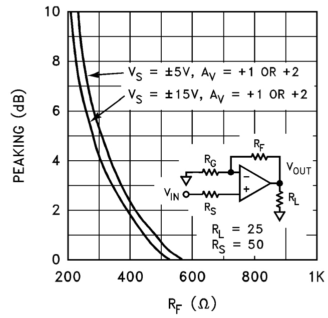


Typical Performance Curves (Continued)

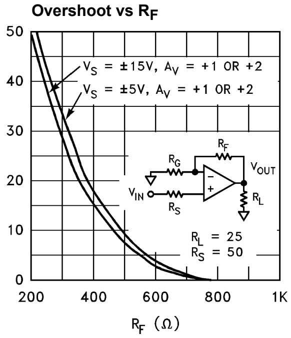
-3dB Bandwidth vs R_F



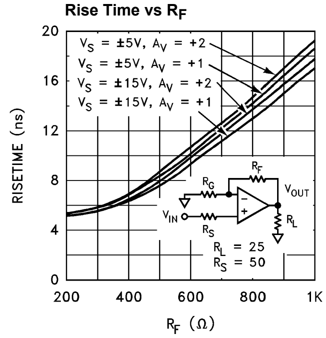
Peaking vs R_F



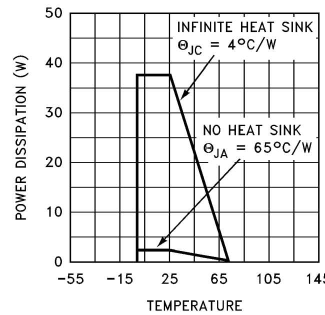
Overshoot vs R_F



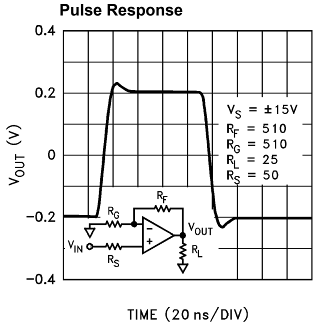
Rise Time vs R_F



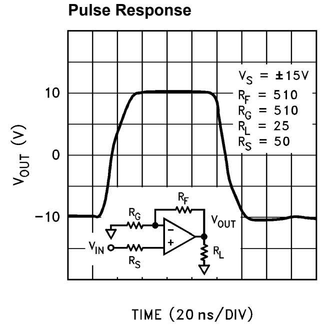
5-Pin TO-220 Maximum Power Dissipation vs Ambient Temperature



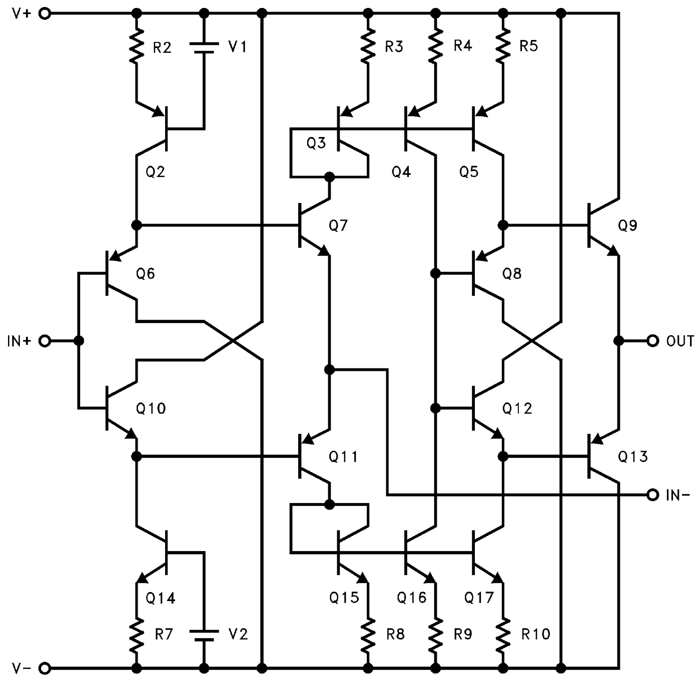
Small Signal Pulse Response



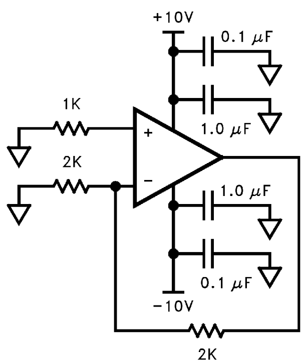
Large Signal Pulse Response



Simplified Schematic



Burn-In Circuit



Applications Information

Product Description

The EL2099 is a current mode feedback amplifier that has high output current drive capability. It is built using Elantec's proprietary dielectric isolation process that produces NPN and PNP complimentary transistors. The high output current can be useful to drive many standard video loads in parallel, as well as digital sync pulses that are 8V or greater.

+Input Resistor Value

A small value resistor located in the +Input pin is necessary to keep the EL2099 from oscillating under certain conditions. A 50Ω resistor is recommended for all applications, although smaller values will work under some circumstances. All tests listed in this datasheet are performed with 50Ω in the +Input pin, as well as all typical performance curves. The 50Ω resistor along with the +Input bias current creates an additional typical Offset Voltage of only 250μV at T = 25°C, and a maximum of 1.25mV over temperature variations.

Feedback Resistor Values

The EL2099 has been designed and specified with R_F = 510Ω and A_V = +2. This value of feedback resistor yields extremely flat frequency response with little to no peaking. However, 3dB bandwidth is reduced somewhat because of this. Wider bandwidth, at the expense of slight peaking, can be accomplished by reducing the value of the feedback resistor. For example, at a gain of +2, reducing the feedback resistor to 330Ω increases the -3dB bandwidth to 70MHz with 3dB of peaking. Inversely, larger values of feedback resistor will cause roll off to occur at a lower frequency. There is essentially no peaking with R_F > 510Ω.

Power Supplies

The EL2099 may be operated with single or split supplies as low as ±5V (10V total) to as high as ±18V (36V total). Bandwidth and slew rate are almost constant from V_S = ±10V to ±18V, and decrease slightly as supplies are reduced to ±5V, as shown in the characteristic curves. It is not necessary to use equal value split supplies. For example, -5V and -12V would be fine for 0V to 1V video signals.

Good power supply bypassing should be used to reduce the risk of oscillation. A 1µF to 10µF tantalum capacitor in parallel with a 0.1µF ceramic capacitor is recommended for bypassing each supply pin. They should be kept as close as possible to the device pins.

Due to the internal construction of the TO-220 package, the tab of the EL2099 is connected to the V_S- pin. Therefore, care must be taken to avoid connecting the tab to the ground plane of the system.

Printed Circuit Board Layout

As with any high frequency device, good printed circuit board layout is necessary for optimum performance. Ground plane construction is highly recommended. Pin lengths should be as short as possible. For good AC performance, parasitic capacitances should be kept to a minimum, especially at the inverting input, which is sensitive to stray capacitance. This implies keeping the ground plane away from this pin. Metal film and carbon resistors are both acceptable, while use of wire-wound resistors is not recommended because of their parasitic inductance. Similarly, capacitors should be low inductance for best performance.

Driving Cables and Capacitive Loads

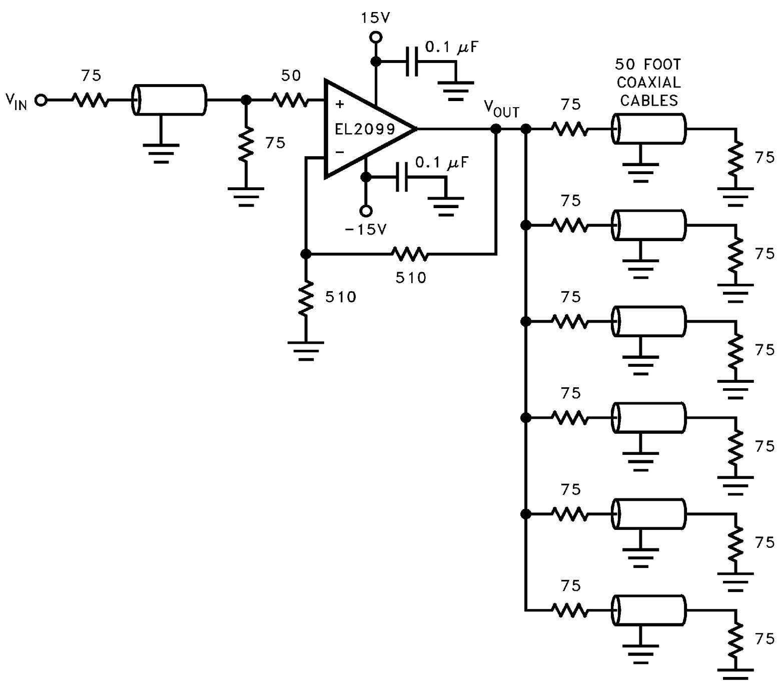
The EL2099 was designed with driving multiple coaxial cables in mind. With 440mA of output drive and low output impedance, driving six, 75Ω double terminated coaxial cables to ±11V with one EL2099 is practical.

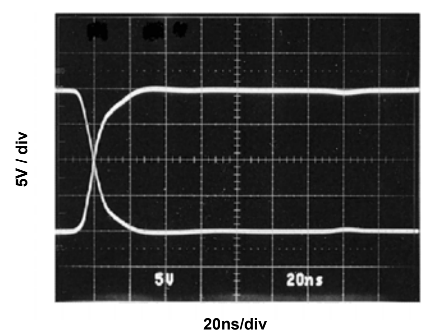
When used as a cable driver, double termination is always recommended for reflection-free performance. For those applications, the back termination series resistor will decouple the EL2099 from the capacitive cable and allow extensive capacitive drive. For a discussion on some of the other ways to drive cables, see the application section on driving cables in the EL2003 data sheet.

Other applications may have high capacitive loads without termination resistors. In these applications, an additional small value (5Ω-50Ω) resistor in series with the output will eliminate most peaking.

The schematic below shows the EL2099 driving 6 double terminated cables, each of average length of 50 feet.

This represents driving an effective load of 25Ω to over ±10V. The resulting performance is shown in the scope photo. Notice that double termination results in reflection free performance.





EL2099 Macromodel

```
* Connections:    +input
*                   |     -input
*                   |       |     +Vsupply
*                   |       |       |     -Vsupply
*                   |       |       |       |     output
*                   |       |       |       |       |
.subckt M2099       4       5       1       3       2
*
* Input Stage
*
e1 10 0 4 0 1.0
vis 10 9 0V
h2 9 12 vxx 1.0
r1 5 11 50
l1 11 12 48nH
iinp 4 0 5µA
iinm 5 0 -8µA
*
* Slew Rate Limiting
*
h1 13 0 vis 600
r2 13 14 1K
d1 14 0 dclamp
d2 0 14 dclamp
*
* High Frequency Pole
*
*e2 30 0 14 0 0.001667
13 30 17 1.5µH
c5 17 0 1pF
r5 17 0 500
*
* Transimpedance Stage
*
g1 0 18 17 0 1.0
ro1 18 0 150K
cdp 18 0 8pF
*
* Output Stage
*
q1 3 18 19 qp
q2 1 18 20 qn
q3 1 19 21 qn
q4 3 20 22 qp
r7 21 2 1
r8 22 2 1
ios1 1 19 5mA
ios2 20 3 5mA
*
* Supply Current
*
ips 1 3 19mA
*
* Error Terms
*
ivos 0 23 5mA
vxx 23 0 0V
e4 24 0 2 0 1.0
e5 25 0 1 0 1.0
```

EL2099 Macromodel (Continued)

```
e6 26 0 3 0 1.0
r9 24 23 3K
r10 25 23 1K
r11 26 23 1K

* Models
*
.model qn npn (is=5e-15 bf=200 tf=0.1nS)
.model qp pnp (is=5e-15 bf=200 tf=0.1nS)
.model dclamp d (is=1e-30 ibv=0.266 bv=5 n=4)
.ends
```