

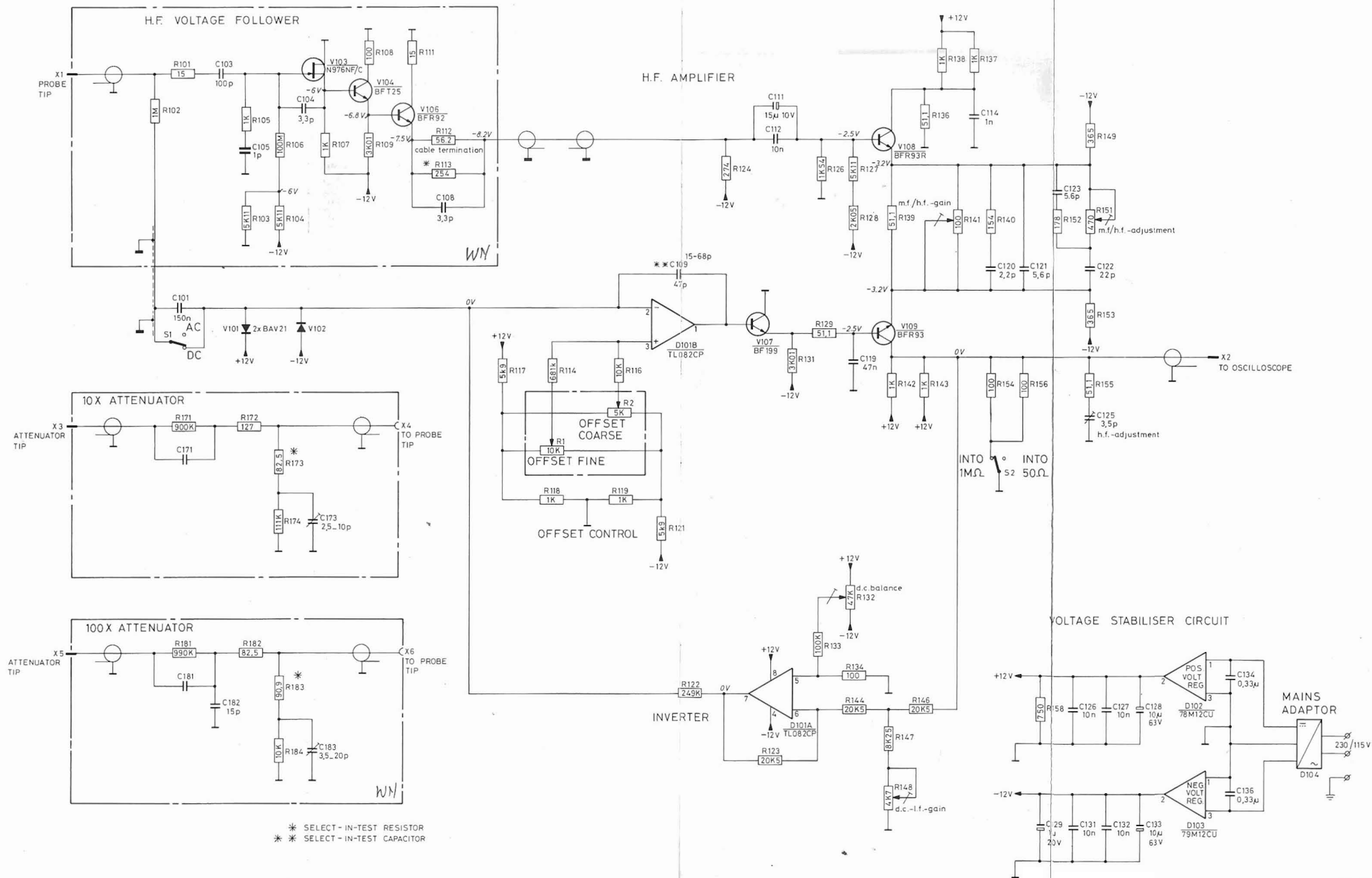


Fig. 23. Overall circuit diagram